

8
8**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In regard to the application of: Amnon GANOT, et al.

Serial No : 10/045,651

Group Art Unit: 2825

Filed : November 7, 2001

Examiner: Unknown

For : Multi-Layer Printed Circuit Board Fabrication System and Method

**INFORMATION DISCLOSURE STATEMENT**

Hon. Commissioner of Patents and Trademarks

Washington, DC 20231

Sir:

Applicants respectfully direct the attention of the Examiner to art cited with respect to U.S. Application No. 10/045,651 which is a continuation-in-part of U.S.S.N. 09/792,498, which is a continuation-in-part of U.S.S.N. 09/708,160.

The Examiner is respectfully requested to review and consider this art, in accordance with MPEP 2001.06 and to indicate in the first office action that he has considered this art. Additionally, the Examiner is respectfully requested to cite those prior art publications mentioned in this application which the examiner considers to be material or relevant to the present claims.

Further, in order to comply with discretionary regulations 37 C.F.R. §1.97 and §1.98, attached is Form PTO-1449 listing the cited art. This art contains information which the Examiner may consider to be important in deciding whether to allow the present application to issue as a patent. Applicants wish to point out that a copy of listed item, WO 00/02424, cited in parent case U.S.S.N. 09/792,498, is not attached as it is available in the parent file.

In accordance with MPEP Section 609 it is requested that each document cited including any mentioned in Applicant's specification which is not repeated on the attached PTO-1449 form be given thorough consideration and that it be cited of record in the prosecution history of the present application by initialing on PTO-1449 form, so that it will appear on the face of the patent issuing on the present application, even if the Examiner does not consider it sufficiently pertinent to use in a rejection, or otherwise does not believe that the guidelines for citation have been fully complied with.

The present Information Disclosure Statement is being submitted in compliance with 37 C.F.R. §1.56 as an Examiner might consider any cited document important in deciding whether to allow the application to issue as a patent, but the citation of each document is not to be construed as an admission that such document is necessarily relevant or prior art. No representation is intended that the cited documents represent the results of a complete search, and it is anticipated that the Examiner in the normal course of examination, will make an independent search and will determine the best prior art consistent with 37 C.F.R. §1.104(a), and in the course of such search will review for relevance every document cited on the attached form even if not initialed.

Early and favorable consideration is earnestly solicited.

Respectfully submitted,
Amnon GANOT, et al.



Paul Fenster,
Registration No. 33,877

May 26, 2002

William H. Dippert, Esq.
Reed Smith LLP
375 Park Avenue
New York, NY 10152-1799

Tel: (212) 521-5400

(Use several sheets if necessary)

085/02455

10/045,651

Amnon GANOT, et al.

November 7, 2001

2825

[illegible][illegible]

DATE CONSIDERED

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP Section 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.